

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

5 Applicants: Jiunn-Ren Hwang et al.
Serial No.: 09/851,580 Filing Date: 05/10/2001
Examiner: RUGGLES, JOHN S Art Unit: 1756
Docket No.: NAUP0292USA

10 Title: METHOD OF FORMING STORAGE NODES IN A DRAM

To: Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

15

Subject: Status Inquiry

Dear Sir,

20 The applicant submitted a petition for revival of the
above-identified application on 11/18/2004 and had received a notice
mailed on 01/24/2005 indicating that the petition was granted
(Attachment 1). However, the applicant recently found that: (1) the
status of the above-identified application remains "abandoned" in PAIR
25 (Attachment 2); (2) the petition decision cannot be retrieved on the
image file wrapper; and (3) there is no further progress of the
above-identified application for over two years.

30 The applicant hereby requests the USPTO to: (1) update the status
for the above-identified application in PAIR; (2) accelerate the issuance
of the latest office action. Your quick response in this matter is greatly
appreciated.

Respectfully submitted,

Winston Hsu

Date: OCT-08 2007

- 5 Winston Hsu, Patent Agent No. 41,526
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- 10 (Note: Note: Please leave a message in my voice mail if you need to talk
to me. The time in D.C. is 12 hours behind the Taiwan time, i.e. 9 AM in
D.C. = 9 PM in Taiwan)



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Attachment 1

(NAIPC) North America International Patent Office
P. O. Box 506
Merrifield, VA 22116

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OFFICE OF PETITIONS

In re Application of
Jiunn-Ren Hwang, et al.
Application No. 09/851,580
Filed: May 10, 2001
Attorney Docket No. NAUP0292USA

ON PETITION

This is a decision on the petition under 37 CFR 1.137(b), filed November 18, 2004, to revive the above-identified application.

The petition is **GRANTED**.

Telephone inquiries concerning this decision should be directed to the undersigned at (571) 272-3223.

The application file is being referred to Technology Center 1700.

Marianne E. Jenkins
Petitions Examiner
Office of Petitions
Office of the Deputy Commissioner
for Patent Examination Policy

Attachment 2

09/851,580 Method of forming storage nodes in a DRAM 10-05-2007::05:22:34

Bibliographic Data

Application Number:	09/851,580	Customer Number:	-
Filing or 371 (c) Date:	05-10-2001	Status:	Abandoned -- Failure to Respond to an Office Action
Application Type:	Utility	Status Date:	10-21-2004
Examiner Name:	RUGGLES, JOHN S	Location:	ELECTRONIC
Group Art Unit:	1756	Location Date:	-
Confirmation Number:	3189	Earliest Publication No:	US 2002-0168590 A1
Attorney Docket Number:	NAUP0292USA	Earliest Publication Date:	11-14-2002
Class / Subclass:	430/311	Patent Number:	-
First Named Inventor:	Jiunn-Ren Hwang , Tai-Chung City, (TW)	Issue Date of Patent:	-

Title of Invention: Method of forming storage nodes in a DRAM

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